

UNITED STATES PATENT APPLICATION

OF

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FOR

**VERTICAL CAVITY SURFACE EMITTING LASER HAVING A GAIN GUIDE
APERTURE INTERIOR TO AN OXIDE CONFINEMENT LAYER**

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CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] Not applicable.

UNITED STATES GOVERNMENT RIGHTS

[0002] The United States Government has acquired certain rights in this invention through Government Contract No. 70NANB5H1114 awarded by NIST ATP.

BACKGROUND OF THE INVENTION

Field of the Invention

[0003] The present invention relates generally to the field of vertical cavity surface emitting lasers. More specifically, it relates to current confinement structures used in vertical cavity surface emitting lasers.

Discussion of the Related Art

[0004] Vertical cavity surface emitting lasers (VCSELs) represent a relatively new class of semiconductor lasers. While there are many variations of VCSELs, one common characteristic is that they emit light perpendicular to a wafer's surface. Advantageously, VCSELs can be formed from a wide range of material systems to produce specific characteristics.

[0005] VCSELs include semiconductor active regions, which can be fabricated from a number of material systems, distributed Bragg reflector (DBR) mirrors, current confinement structures, substrates, and contacts. Because of their complicated structure, and because of their material requirements, VCSELs are

usually grown using metal-organic chemical vapor deposition (MOCVD) or by molecular beam epitaxy (MBE).

[0006] Figure 1 illustrates a typical VCSEL 10. As shown, an n-doped gallium arsenide (GaAs) substrate 12 has an n-type electrical contact 14. An n-doped lower mirror stack 16 (a DBR) is on the GaAs substrate 12, and an n-type graded-index lower spacer 18 is disposed over the lower mirror stack 16. An active region 20, usually having a number of quantum wells, is formed over the lower spacer 18. A p-type graded-index top spacer 22 is disposed over the active region 20, and a p-type top mirror stack 24 (another DBR) is disposed over the top spacer 22. Over the top mirror stack 24 is a p-type conduction layer 9, a p-type cap layer 8, and a p-type electrical contact 26.

[0007] Still referring to Figure 1, the lower spacer 18 and the top spacer 22 separate the lower mirror stack 16 from the top mirror stack 24 such that an optical cavity is formed. As the optical cavity is resonant at specific wavelengths, the mirror separation is controlled to resonant at a predetermined wavelength (or at a multiple thereof). At least part of the top mirror stack 24 includes an insulating region 40 that provides current confinement. The insulating region 40 is usually formed either by implanting protons into the top mirror stack 24 or by forming an oxide layer. The insulating region 40 defines a conductive annular central opening 42 that forms an electrically conductive path through the insulating region 40.

[0008] In operation, an external bias causes an electrical current 21 to flow from the p-type electrical contact 26 toward the n-type electrical contact 14. The insulating region 40 and the conductive central opening 42 confine the current 21

such that the current flows through the conductive central opening 42 and into the active region 20. Some of the electrons in the current 21 are converted into photons in the active region 20. Those photons bounce back and forth (resonate) between the lower mirror stack 16 and the top mirror stack 24. While the lower mirror stack 16 and the top mirror stack 24 are very good reflectors, some of the photons leak out as light 23 that travels along an optical path. Still referring to Figure 1, the light 23 passes through the p-type conduction layer 9, through the p-type cap layer 8, through an aperture 30 in the p-type electrical contact 26, and out of the surface of the vertical cavity surface emitting laser 10.

[0009] It should be understood that Figure 1 illustrates a typical VCSEL, and that numerous variations are possible. For example, the dopings can be changed (say, by providing a p-type substrate 12), different material systems can be used, operational details can be tuned for maximum performance, and additional structures, such as tunnel junctions, can be added.

[0010] While generally successful, VCSELs have problems. For example, in some applications it is important for a VCSEL to emit light in the fundamental mode. That is, light with a single unimodal spatial mode (from only one area of the VCSEL) and with a single spectral content. However, prior art insulating regions 40 are less than optimal in producing single mode light. To understand why this is so, the insulation region 40 needs to be understood in more detail.

[0011] As noted, the insulating region 40 and the central opening 42 act as a current guide into the active region. Also as noted, the insulating region is usually produced either by implanting protons or by forming an oxide layer. Proton

implantation is described by Y. H. Lee et al., *Electr. Lett.*, Vol. 26, No. 11, pp. 710-711 (1990) and by T. E. Sale, *Vertical Cavity Surface Emitting Lasers*, Research Press Ltd., pp. 117-127 (1995), both of which are incorporated by reference. Oxide layers are taught by D. L. Huffaker et al., *Appl Phys. Lett.*, Vol. 65, No. 1, pp. 97-99 (1994) and by K. D. Choquette et al., *Electr. Lett.*, Vol. 30, No.24, pp. 2043-2044 (1994), both of which are incorporated by reference.

[0012] Ion-implanted VCSELs are typically fabricated using a single energy proton implant in the shape of an annular ring to define a current aperture (multiple implant energies are used to electrically isolate the entire VCSEL). That proton implantation creates structural defects that produce a relatively high resistance annular structure having a conductive core. While the high resistance annular structure effectively steers current through its conductive core and into the active region, ion implantation does not produce significant optical guiding. The result is that ion implantation is effective at steering current into the active region, but ineffective at limiting the optical modes of the laser. Thus, prior art ion implanted VCSELs tended to operate with multiple lasing modes.

[0013] In contrast, VCSELs that use oxide current confinement regions benefit from the oxide layer's optical index of refraction, which is about half that before oxidation. This forms an optical guide that tends to provide transverse mode optical confinement. However, because of the distributed nature of the series resistance, oxide VCSELs have the highest P-N junction current density and the highest optical gain at the edge of the cavity. This current distribution encourages the formation of higher order spatial modes, particularly at large bias currents. While the transverse mode optical confinement suppresses undesirable higher order optical

modes, in the prior art to obtain single fundamental mode operation an oxide optical current confinement region had to have such a small aperture that light from the VCSEL was severally reduced.

[0014] Oxide VCSELs (those that use oxide current confinement) typically include an AlGaAs layer with a high aluminum content (97-98%). Such a high aluminum content structure tends to oxidize much more rapidly than the material layers used to form the rest of the P-type DBR mirror (which is typically 85% Al and 15% Ga). To fabricate the oxide current confinement, reactive ion etching is used to form trenches to the edge of a high Al content layer. Oxidation then forms about a 10 micron deep oxide structure in the high Al content layer, while forming less than a 1 micron deep oxide structure in the adjacent layers. The high Al content layer oxidizes with a complex aluminum oxide that is not only an electrical insulator, but also which occupies about the same space as the layer before oxidation.

[0015] Because oxide VCSELs and ion-implanted VCSELs have different characteristics, VCSEL designers have had to chose from among competing features, high output power with higher order spectral modes (oxide VCSELs), or lower output power but with fewer spatial modes (ion implanted VCSEL). Therefore, a new technique of forming VCSELs with the benefits of both ion implanted VCSELs and oxide VCSELs would be beneficial.

SUMMARY OF THE INVENTION

[0016] The following summary of the invention is provided to facilitate an understanding of some of the innovative features unique to the present invention, and is not intended to be a full description. A full appreciation of the various aspects of

the invention can be gained by taking the entire specification, claims, drawings, and abstract as a whole.

[0017] Accordingly, the principles of the present invention are directed to a new VCSEL that possess benefits of both ion implanted and oxide VCSELs. According to the principles of the present invention, an oxide structure and an interior gain guide ion implant structure are combined to form a VCSEL. The combination of the oxide layer and an interior ion implant gain guide can achieve desirable low order optical modes with low threshold and high efficiency. To support fewer optical modes the oxide layer is formed more than two mirror periods from the active region. Beneficially, the oxide layer has an optical thickness less than $\frac{1}{4}$ wavelength.

[0018] The novel features of the present invention will become apparent to those of skill in the art upon examination of the following detailed description of the invention or can be learned by practice of the present invention. It should be understood, however, that the detailed description of the invention and the specific examples presented, while indicating certain embodiments of the present invention, are provided for illustration purposes only because various changes and modifications within the spirit and scope of the invention will become apparent to those of skill in the art from the detailed description of the invention and claims that follow.

BRIEF DESCRIPTION OF THE DRAWING

[0019] The accompanying figures, in which like reference numerals refer to identical or functionally-similar elements throughout the separate views and which are incorporated in and form part of the specification, further illustrate the present

invention and, together with the detailed description of the invention, serve to explain the principles of the present invention.

[0020] In the drawings:

[0021] Figure 1 illustrates a typical vertical cavity surface emitting laser; and

[0022] Figure 2 illustrates a vertical cavity surface emitting laser according to the principles of the present invention.

[0023] Note that in the drawings that like numbers designate like elements. Additionally, for explanatory convenience the descriptions use directional signals such as up and down, top and bottom, and lower and upper. Such signals, which are derived from the relative positions of the elements illustrated in the drawings, are meant to aid the understanding of the present invention, not to limit it.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0024] The principles of the present invention provide for a VCSEL that uses a current confinement structure comprised of an oxide structure and an interior gain guide ion implant structure. An example of such a VCSEL is the VCSEL 100 illustrated in Figure 2. Figure 2 should be understood as a "cut-away" schematic depiction of layers of a VCSEL that is generally configured as shown in Figure 1. Thus, the same element numbers will be used for similar elements in Figure 2 that were used in Figure 1.

[0025] Although for clarity our description of the illustrated embodiment assumes a VCSEL with mirrors and active region based on the AlGaAs material

system, the invention broadly applies to VCSELs using any commensurate material system, such as AlGaAs mirrors and AlGaInP/GaAs active region for 670 nm, or AlGaInAs/InP mirrors and AlGaInAs active region for 1550 nm.

[0026] As shown in Figure 2, the VCSEL 100 includes an n-doped gallium arsenide (GaAs) substrate 12 having an n-type electrical contact 14. An n-doped lower mirror stack 16 (a DBR) is on the GaAs substrate 12, and an n-type graded-index lower spacer 18 is disposed over the lower mirror stack 16.

[0027] An active region 20 having P-N junction structures and a number of quantum wells is formed over the lower spacer 18. The composition of the active region 20 is beneficially AlGaAs, with the specific aluminum concentration varying in the layers that form the active region 20. For example, one layer may have between twenty and thirty percent of aluminum, while an adjacent layer might have between zero and five percent of aluminum. There could be more or fewer alternating layers, depending on how the quantum wells are to be arranged in the active region 20. Over the active region 20 is a p-type graded-index top spacer 22. A p-type top mirror stack 24 (another DBR) is disposed over the top spacer 22. Over the top mirror stack 24 is a p-type conduction layer 9, a p-type GaAs cap layer 8, and a p-type electrical contact 26. As in the VCSEL 10 (see Figure 1), the lower spacer 18 and the top spacer 22 separate the lower mirror stack 16 from the top mirror stack 24 such that an optical cavity that is resonant at a specific wavelength is formed. More about this subsequently.

[0028] Still referring to Figure 2, the top mirror stack 24, and part of the top spacer 22, includes an oxide insulating region 140. That oxide insulating region is produced by forming the top mirror stack 24 with an AlGaAs layer having a high

aluminum content (97-98%), and then oxidizing that high aluminum content layer along an annular ring. Oxidation then produces the oxide insulating region 140. Then, a spatial region 180 is implanted with ions 160. The implanted ions form a high resistance region within the annular ring formed by the oxide layer 140. The implanted spatial region 180 should be in close proximity to the oxide insulating region 140. For example, the spatial region 180 may be positioned coincident with, somewhat above, or somewhat below the region 140. This enables optimization of the electrical and optical properties for a specific application.

[0029] In operation, an external bias causes an electrical current 121 to flow from the p-type electrical contact 26 toward the n-type electrical contact 14. The oxide insulating region 140 and the ion implanted spatial region 180 guide the current 121 through a conductive central opening such that the current 121 flows into the active region 20. Some of the electrons in the current 121 are converted into photons in the active region 20. Those photons bounce back and forth (resonate) between the lower mirror stack 16 and the top mirror stack 24. While the lower mirror stack 16 and the top mirror stack 24 are very good reflectors, some of the photons leak out as light 23 that travels along an optical path. Still referring to Figure 2, the light 23 passes through the p-type conduction layer 9, through the p-type GaAs cap layer 8, through an aperture 30 in the p-type electrical contact 26, and out of the surface of the vertical cavity surface emitting laser 100.

[0030] Several features of the VCSEL 100 should be highlighted. First, the lower spacer 18 and the top spacer 22 separate the lower mirror stack 16 from the top mirror stack 24 by at least one wavelength in optical path of the light 23. Furthermore, the oxide layer 140 beneficially has an optical path of less than $\frac{1}{4}$ wavelength. Both of

those features reduce the effective index difference, which provides small optical mode confinement, and which tends to suppress higher order optical modes. Also, as shown in Figure 2, the oxide layer 140 is more than two mirror periods from the active region. Additionally, the ion implanted spatial region 180 is beneficially concentrically aligned with the oxide insulating region 140. The ion implanted spatial region 180 and the oxide insulating region 140 then jointly act to confine electrical current 121 within a small aperture region. Furthermore, the oxide insulating region 140 acts to confine the lasing light along a defined optical path. Thus, optical gain is preferentially given to optical modes having a large fraction of intensity in the small aperture region, and the fundamental mode always satisfies that criterion. Thus, single mode (fundamental) operation can be achieved.

[0031] The oxide insulating region 140 is beneficially introduced at the proper location and thickness to achieve the best optical results, while the energy and dose of the implanted ions 60 can be controlled to tailor the lateral sheet resistance. Proper VCSEL design enables achievement of a low order optical mode structure with low threshold and high efficiency.

[0032] The embodiments and examples set forth herein are presented to best explain the present invention and its practical application and to thereby enable those skilled in the art to make and utilize the invention. Those skilled in the art, however, will recognize that the foregoing description and examples have been presented for the purpose of illustration and example only. Other variations and modifications of the present invention will be apparent to those of skill in the art, and it is the intent of the appended claims that such variations and modifications be covered. The description as set forth is not intended to be exhaustive or to limit the scope of the

invention. Many modifications and variations are possible in light of the above teaching without departing from the spirit and scope of the following claims. It is contemplated that the use of the present invention can involve components having different characteristics. It is intended that the scope of the present invention be defined by the claims appended hereto, giving full cognizance to equivalents in all respects.